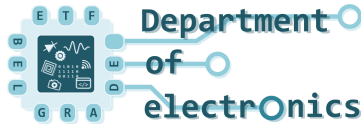




University of Belgrade
School of
Electrical Engineering



Research Group for
Signal Processing
Algorithms and
Architectures

Junior Research Assistant

One position available

Research group for Signal Processing Algorithms and Architectures (SPARC) is looking for Junior Research Assistant to work on projects in the field of VLSI signal processing. We are looking for a creative, flexible, and self-motivated individual with excellent communications skills, and a passion for problem solving in a fast-paced team environment.

What do we offer?

- Research and development position
- A two years full-time contract with industry competitive salaries
- An opportunity to work on cutting edge technologies with particular focus on hardware FPGA based accelerators for 5G signal processing and channel coding
- Strong connections with our industry partners
- Opportunity to publish research papers and pursue a Ph.D. degree in Electrical Engineering

Qualifications/Requirements

- BS/MS in Electrical Engineering
- Strong theoretical background in digital electronics
- Fundamental background in digital design using hardware description languages
- Basic knowledge of FPGA architecture
- Fundamental understanding of digital signal processing (DSP) algorithms
- One or more of the following is a plus:
 - Practical experience in VLSI design for FPGA and/or ASIC
 - Practical experience in development and/or implementation of DSP algorithms
 - Strong Python and/or C/C++ programming skills
 - Experience in working on a research project
 - Ph.D. student at University of Belgrade
- Good verbal and written communication skills
- Serbian citizenship

Apply: Send your CV to elmezeni@etf.rs and petrovicv@etf.rs.

For more information about our research, feel free to visit our [website](#) or contact us via email.